

FIG. 1

100



09853809-08401

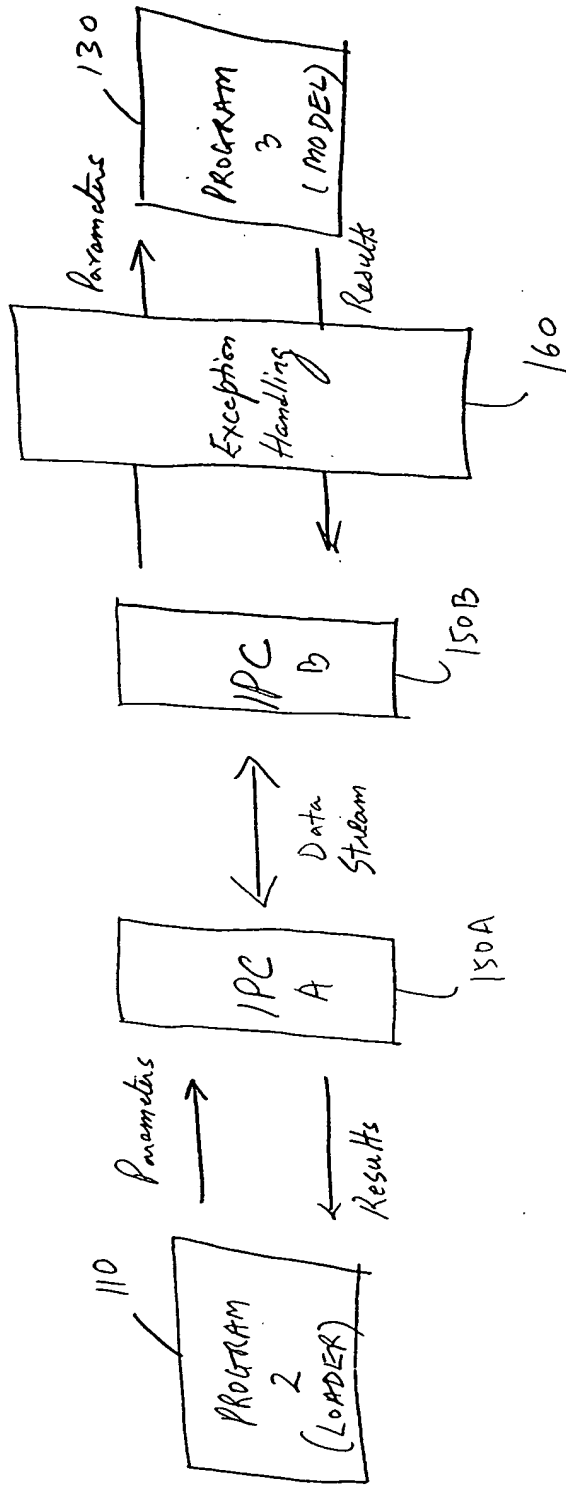


Fig. 2

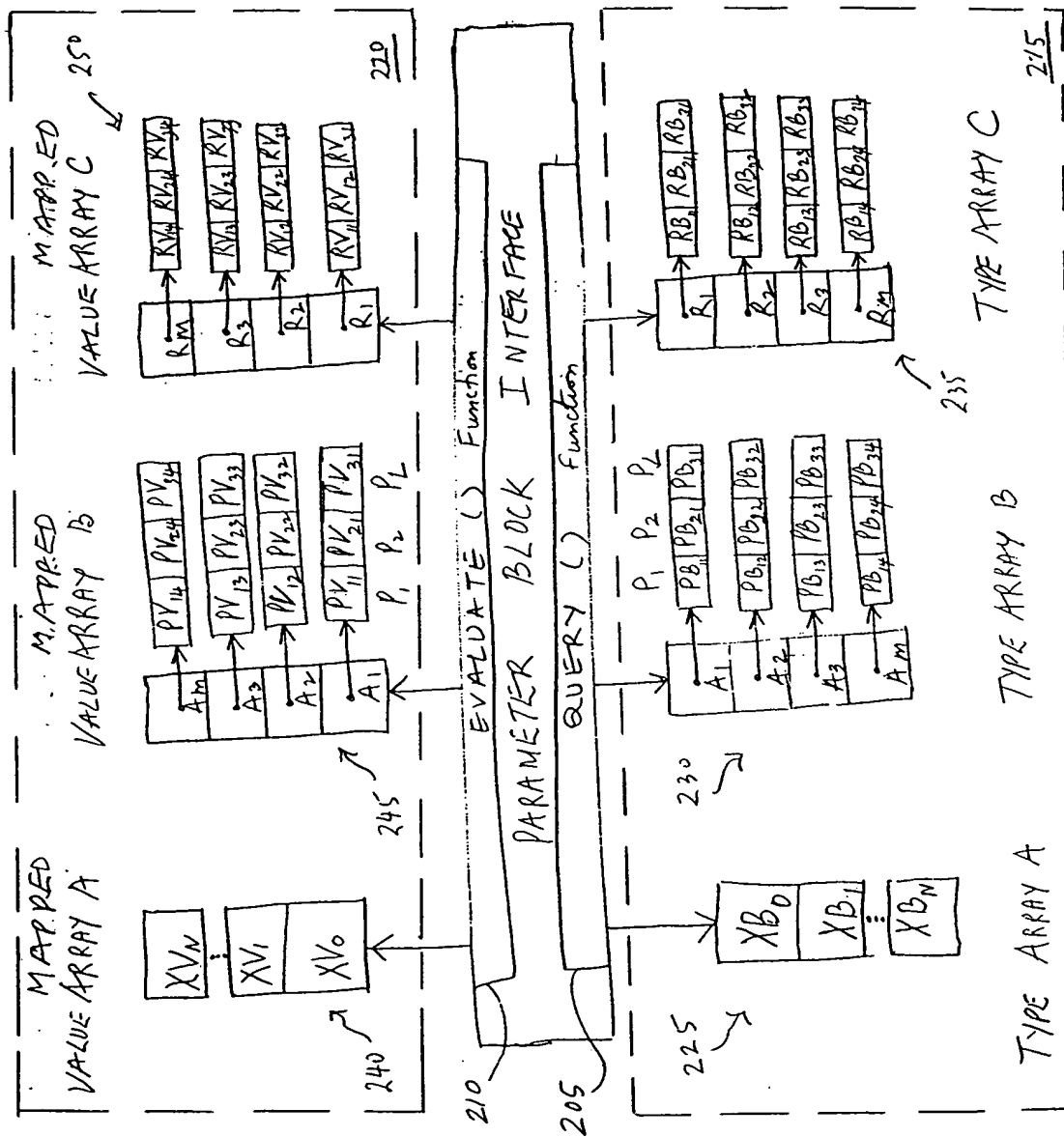


Fig. 3

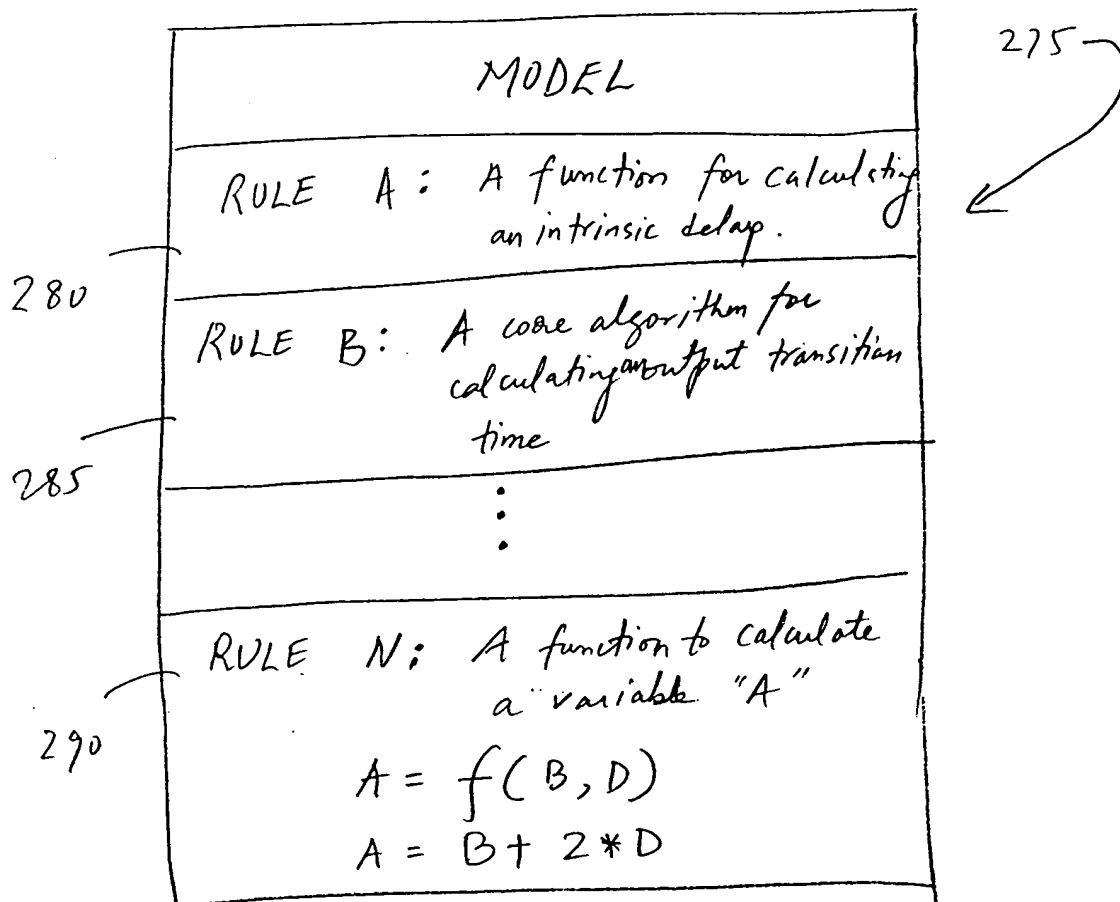
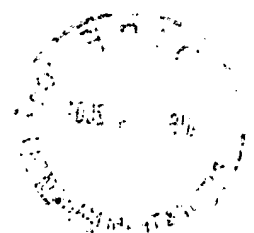


FIG. 4

09863809-032401

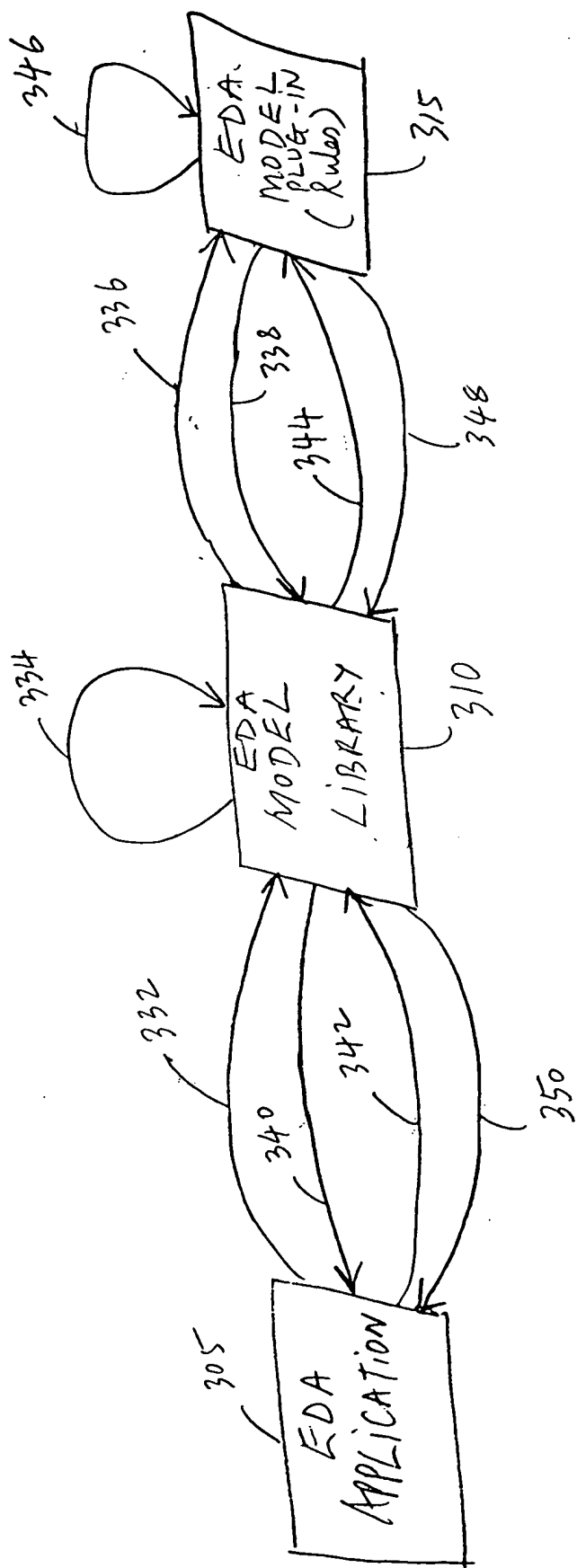


FIG. 5



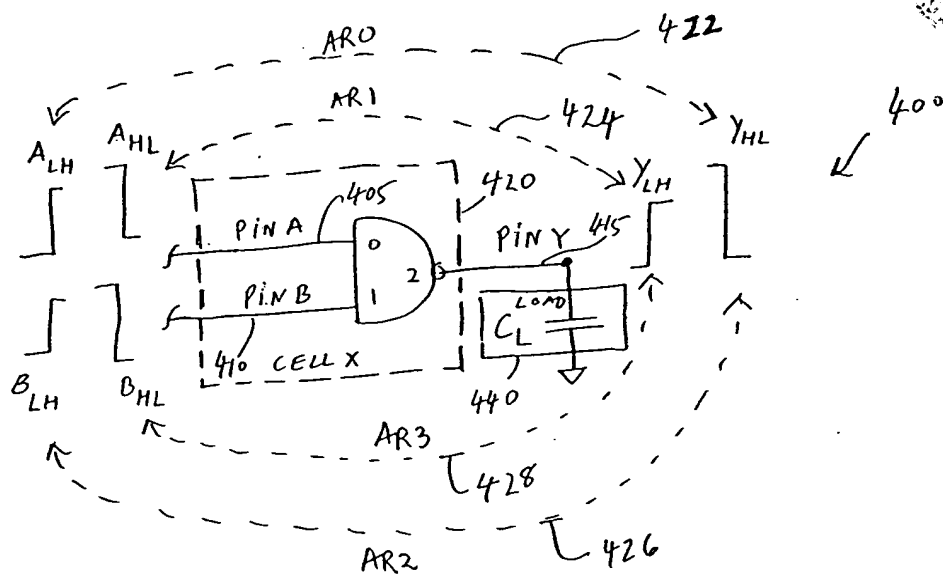


FIG. 6A

ARC RULES	INPUT A.T.		OUTPUT A.T.
	PIN A	PIN B	PIN Y
422 ARO	A _{LH}	1	Y _{HL}
424 AR1	A _{HL}	1	Y _{LH}
426 AR2	1	B _{LH}	Y _{HL}
428 AR3	1	B _{HL}	Y _{LH}

FIG. 6B

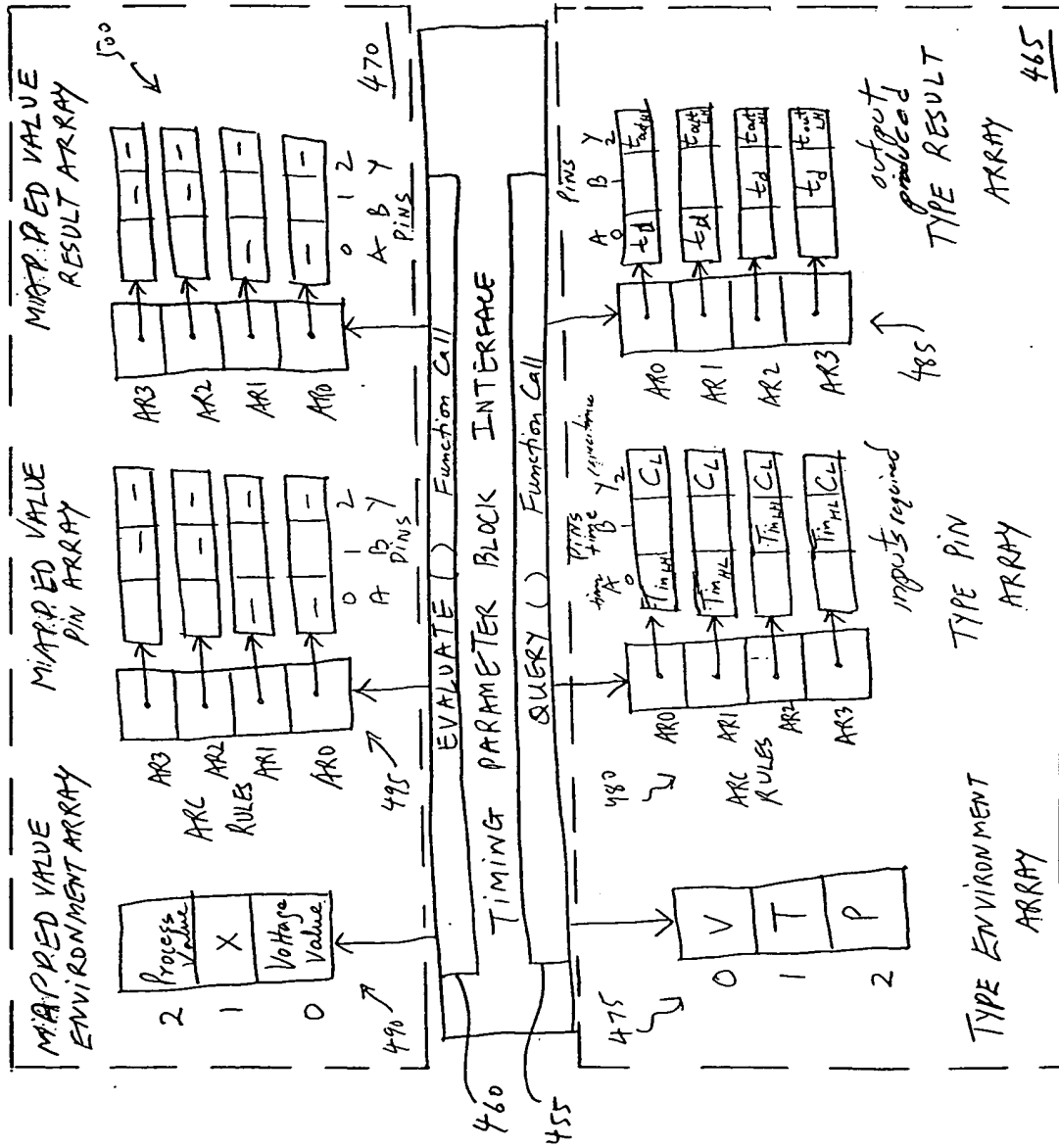


FIG. 7

450

465

460

455

475

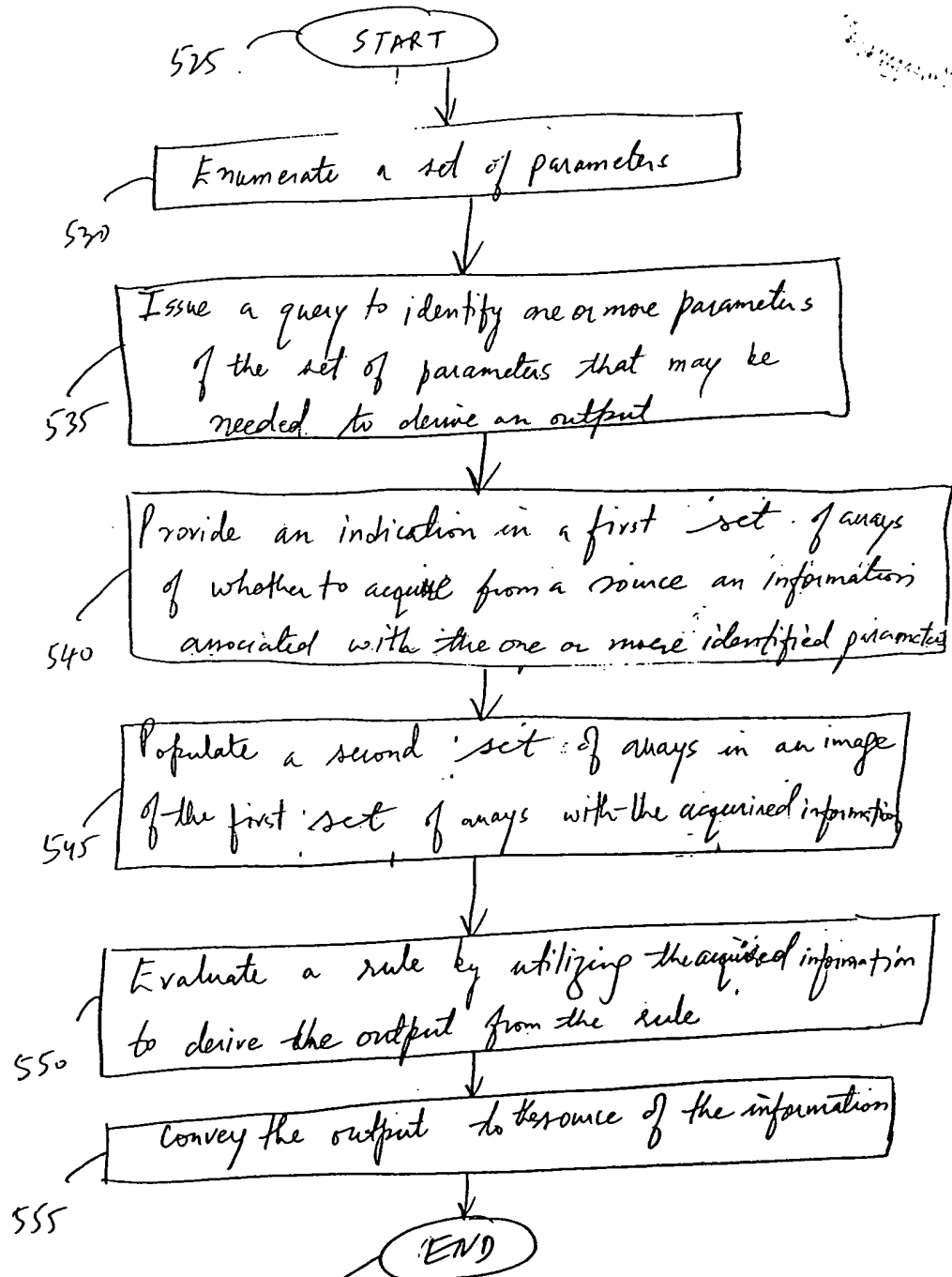
480

485

470

500

09863809.082401



560. FIG. 8A

0063809-082401

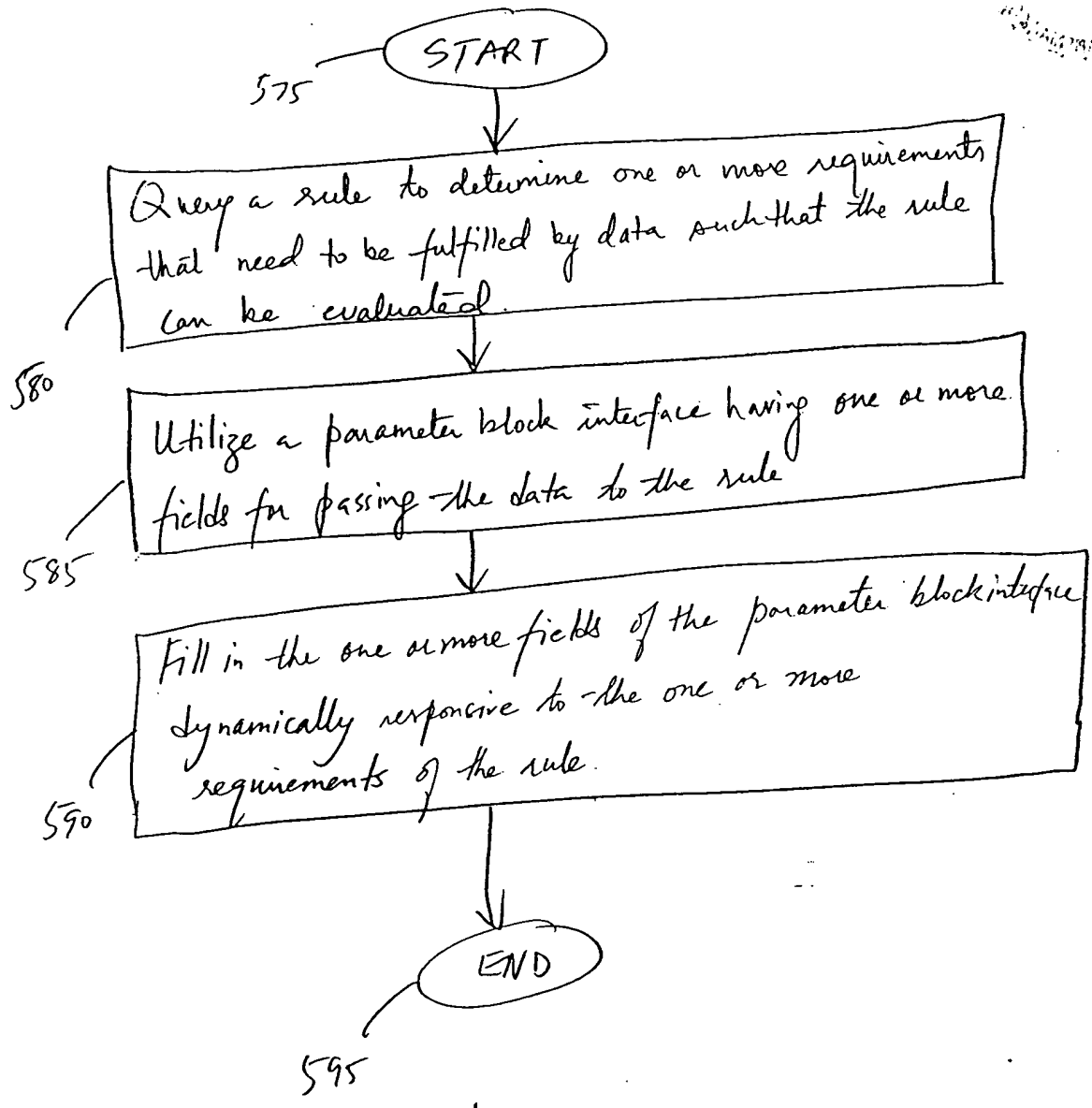


FIG. 8B